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(Use as many sheets as necessary)

Sheet 1 of 1

**Complete if Known**

Application Number	10/777,662
Filing Date	February 13, 2004
First Named Inventor	Makoto Ogawa
Art Unit	Unassigned
Examiner Name	Unassigned
Attorney Docket Number	1237715

**NON PATENT LITERATURE DOCUMENTS**

Examiner Initials *	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
/JP/		ESSCIRC 2003, 29th European Solid-State Circuits Conference, "An Analog Image Processing LSI Employing Scanning Line-Parallel Processing"	X

Examiner Signature	/Jayesh Patel/	Date Considered	04/20/2007
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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